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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

James D. Ballew et al.

Serial No.:

10/824,874

Filing Date:

April 15, 2004

Confirmation No.: Group Art Unit: 7117 2183

Examiner:

George D. Zalepa

Title:

High Performance Computing System and Method

Mail Stop: Amendments Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Dear Sir:

Information Disclosure Statement

Applicants submit this Information Disclosure Statement under 37 C.F.R. § 1.97(c)(2). Applicants respectfully request the Examiner to consider and cite in the examination of this Application the documents listed in the attached Form PTO-1449. Under 37 C.F.R. § 1.98(a)(2)(ii), Applicants have provided copies of all references other than U.S. patents and U.S. patent application publications.

Please charge \$180.00 for the fee set forth in 37 C.F.R. § 1.17(p) to Deposit Account No. 02-0384 of Baker Botts L.L.P. The Commissioner is hereby authorized to charge any fee and credit any overpayment to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted, BAKER BOTTS L.L.P. Attorneys for Applicants

Travis W. Thomas Reg. No. 48,667

Date: March 2, 2007

Correspondence Address:

Customer Number 45507

PTO-1449 Information Disclosure Citation In an Application				Application No. 10/824,874		Applicant(s) James D. Ballew et al.	
				Docket Number 064747.1012	Group Art 2183	Unit	Filing Date April 15, 2004
			U	S. PATENT DOCUMI	ENTS		
		DOCUMENT NO. DATE		NAME	CLASS	SUBCLAS	S FILING DATE
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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	S TRANSLATION
							YES NO
			NO	ON-PATENT DOCUM	ENTS		
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					
	E	E Hans-Ulrich Heiss, "Processor Management in Two-Dimensional Grid-Architectures," Interner Bericht Nr. 20/92, XP002416087, 51 pages					
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